



(19)

(11) Publication number: **63034795 A**

Generated Document.

**PATENT ABSTRACTS OF JAPAN**(21) Application number: **61179756**(51) Intl. Cl.: **G11C 11/34 G11C 11/34**(22) Application date: **29.07.86**

(30) Priority:

(43) Date of application publication: **15.02.88**

(84) Designated contracting states:

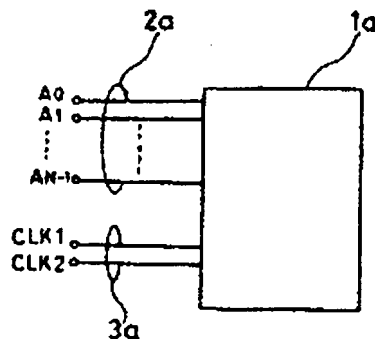
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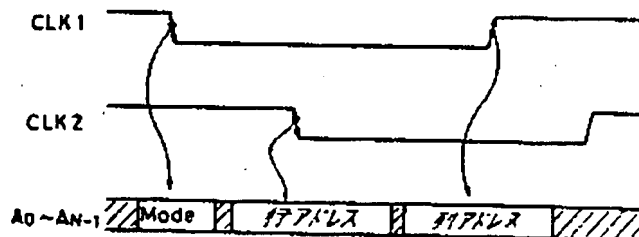
**(54) SEMICONDUCTOR  
STORAGE DEVICE**

(57) Abstract:

**PURPOSE:** To reduce the size of a body and a peripheral device by supplying information relating to the addresses of a storage element which is time-divided into two components or not time-divided and an indication relating to the operation mode of the storage device with time division through address lines.



**CONSTITUTION:** Operation mode information, data input and data output are sent to the address buses 2a with time division from address information A0~AN-1. Namely, the address bus lines for the A0~AN-1 outputs operation mode information, line address information and row address information successively and these information components are triggered at the trailing edge of a CLK 1, the trailing edge of a CLK 2 and the leading edge of the CLK 1 respectively and then applied to the storage device. Thereby, the storage device can execute different operations of 2N based on the mode information. Consequently, the number of input pins of the storage device can be reduced to reduce the size of the storage device and the peripheral device can be simplified and reduced its size.



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